IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Mukunda Krishnappa et al.

Filed: December 15, 2003

For: Apparatus and Methods for Configuration of Programmable Logic

Devices

A1D Plans

Serial No.: 10/736,167

Group Art Unit: 2819

Examiner: Chang, D.

Att'y Docket No.: ALTR:024

Pursuant to 37 C.F.R. § 1.8, I certify that this correspondence is being filed electronically, via facsimile, or deposited with the U.S. Postal Service in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

February 2, 2007

Name

Date

REQUEST FOR AN EXTENSION OF TIME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.136(a), Applicants petition for an extension of time for one month to and including February 5, 2007 (February 4, 2007, falls on a Sunday), in which to respond to the Office Action dated October 4, 2006.

Commissioner for Patents Page 2

Concurrently with filing this paper, the Applicant provides payment electronically. If any other fees are due for any of the enclosed materials, please deduct such fees from (or credit any overpayment to) deposit account number 50-3813/ALTR-024.

Respectfully submitted,

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